Bashirul Azam Biswas

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Lecturer
Department of Electrical and Electronic Engineering
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Educational Qualification

Name of Degree	Institution	Passing Year	GPA/CGPA
S.S.C	Monipur High School, Dhaka	2006	5.00
H.S.C	Dhaka Residential Model College	2008	5.00
B. Sc. in EEE Major- Communication	Bangladesh University of Engineering & Technology	2014	3.76
M. Sc. in EEE (Ongoing) Division – Communication & Electronics (CE)	Bangladesh University of Engineering & Technology	2019 (To be completed)	3.67 (Ongoing)

Project works in Undergraduate & Graduate Courses

Undergraduate Projects

- Design of 4x4 Sudoku, dot matrix display and 8-bit PC in Proteus.
- Power flow analysis of a 9 bus system in PSAF.
- Implementation of watermarking in images
- Pitch detection in speech signals
- Design of a transmitter and receiver operating in high frequency region.
- Implementation of Viterbi algorithm

Software and Programming Skills

MATLAB, PSpice, PSAF, Proteus, MicroC, AVR, Turbo C (C), Visual Basic(C++), Eclipse(Java), AutoCAD, Python, Julia

Graduate Projects

- Estimating random channel impulse response using LMS, VSS-LMS, NLMS and RLS algorithms
- Design of a maternal ECG and EOG canceller using VSS-LMS and an acoustic Echo canceller using RLS
- Recovery of reverberated signal using adaptive beam former and LP residual signal
- Implementation of several spectral estimation methods such as Piseranko, MUSIC, minimum variance, Welch and AR modelling using damped sinusoid of ACF
- Music and speech classification using GMM
- Protein similarity analysis using Kolmogorov complexity
- Non-cooperative iris segmentation by convolutional encoder decoder network
- Protein similarity analysis by wavelet decomposition of cellular automata images

B. Sc. Thesis

- Bashirul Azam Biswas, Shams Shad Islam Khan, S. M. Mahbubur Rahman, "Discriminative masking for non-cooperative IrisCode recognition," in *Proceedings 8th International Conference on Electrical and Computer Engineering*, pp. 124-127, Dhaka, Bangladesh, Dec. 2014.

M. Sc. Thesis (Ongoing)

- Video Summarization using Deep Neural Network

Career History

Industrial Arena

- Physical Chip Design Engineer at PrimeSilicon Technologies Ltd.
- Duration: From September, 2014 to June, 2016.
- List of tapeout projects -
  - 28nm tech node 22x17.3 mm^2 chip of 140M get count
  - 28nm tech node 11x11 mm^2 chip of 57M gate count
- Software Learned –
  - Cadence, Verilog, AtopTech Aprisa, Calibre Physical Verification, Quantus QRC Extraction, Conformal LEC, Tempus Timing Signoff Solution, Unix Environment
- Programming Languages Learned –
  - Perl, TCL/Tk

Academic Arena

- Lecturer at Dept. of EEE, University of Liberal Arts Bangladesh (ULAB).
- Duration: From September, 2017 till now.
- Courses Taught –
  - Electrical Circuits I, Physics, Electric Machines I, Analog & Digital Communication, Control System Engineering, Microwave and Radar Engineering, Wireless and Cellular Communication
- Workshops Attended
  - Training course on Teaching for Active Learning (TAL Batch 3)
  - Julia TOT Workshop on AI/ML
- Workshop Organized
  - Workshop on MATLAB Basics

References

1. **S. M. Mahbubur Rahman, PhD, MIEEE**
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   Dept. of Electrical and Electronic Engineering
   Bangladesh University of Engineering and Technology
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2. **Dr. Mohammed Imamul Hassan Bhuiyan**
   Professor
   Dept. of Electrical & Electronic Engineering
   Bangladesh University of Engineering & Technology
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